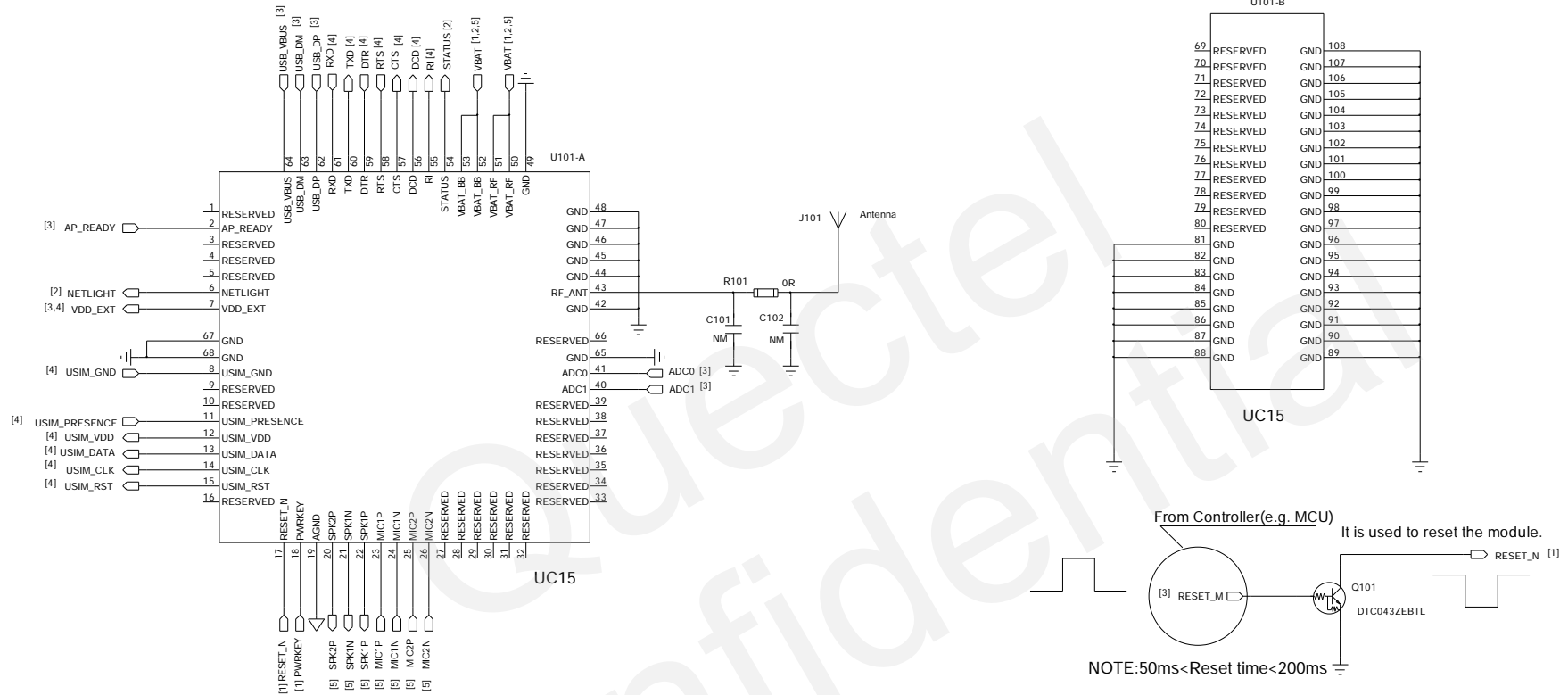
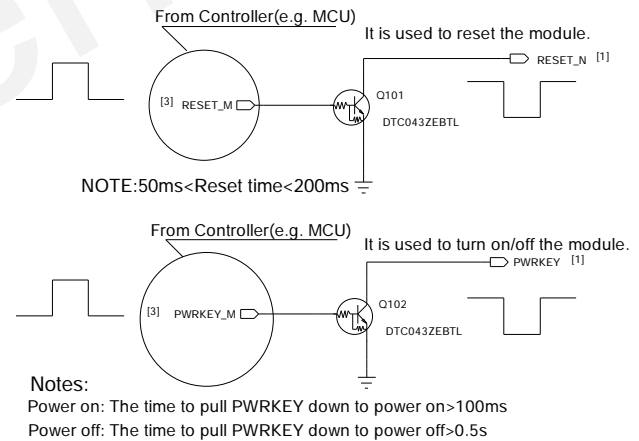


# Module Interface



## Notes:

1. Keep all reserved pins and unused pins unconnected.
2. For detailed information about AP\_READY, please refer to UC15 hardware design.



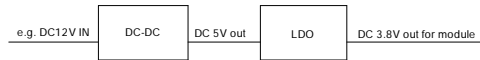
## Quectel Wireless Solutions

DRAWN BY <Hulk.Li>	PROJECT <UC15 Reference Design>	TITLE <Module Interface>
CHECKED BY <Layne.YE>	SIZE A2	VER 1.0
SHEET 1 of 5		<2013.11>

# Power Supply and Indicators Design

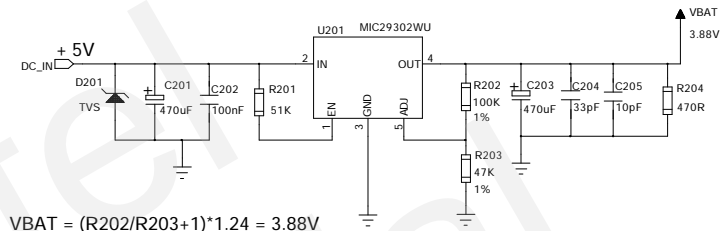
## DC-DC Application

1. It is used when the input voltage is above 9V such as vehicle application.
2. Use DC-DC to convert high input voltage to 5V, and LDO will generate 3.8V typical voltage for the module.



## LDO Application

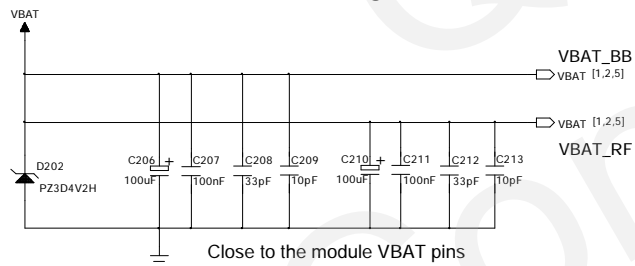
It is used when the input voltage is below 7V.



$$VBAT = (R202/R203+1)*1.24 = 3.88V$$

$$Vmin=3.3V, Vnorm=3.8V, Vmax=4.3V$$

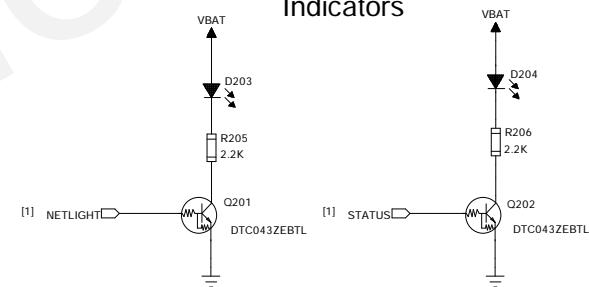
## VBAT Design



Note:

VBAT should be routed in star mode to VBAT\_BB and VBAT\_RF pins.

## Indicators

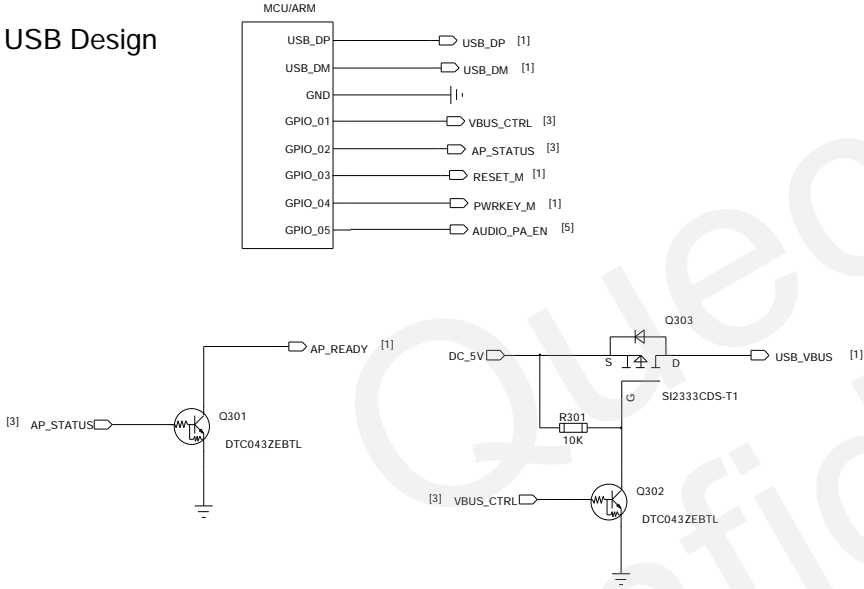


## Quectel Wireless Solutions

DRAWN BY <Hulk.Li>	PROJECT <UC15 Reference Design>	TITLE <Power Supply Design>
CHECKED BY <Layne.YE>	SIZE A2	VER 1.0
SHEET 2 of 5		<2013.11>

# USB and ADC Design

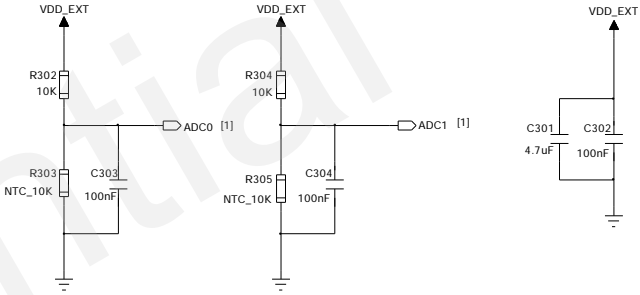
## USB Design



Note:

Q301 is used to realize voltage level translation, and AP\_READY of UC15 should be configured to low level detection.

## ADC Design



Notes:

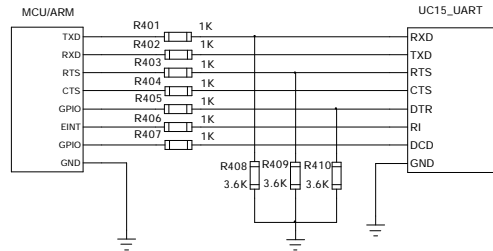
1. Input voltage range of ADC0 and ADC1 channel is 0~2.1V.
2. When VDD\_EXT=2.6V is used to supply power for external circuit, Iout\_max=100mA, it is recommended to add a 4.7uF capacitor.

### Quectel Wireless Solutions

DRAWN BY <Hulk Li>	PROJECT <UC15 Reference Design>	TITLE <USB and ADC Design>
CHECKED BY <Layne YE>	SIZE A2	VER 1.0
SHEET	3 of 5	<2013.11>

# UART and USIM Design

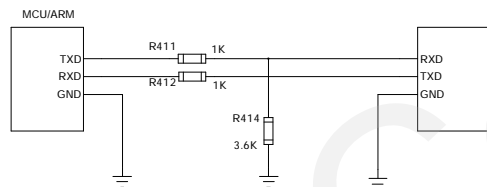
## Connection of All Functional UART Interfaces for 3.3V System



### Notes:

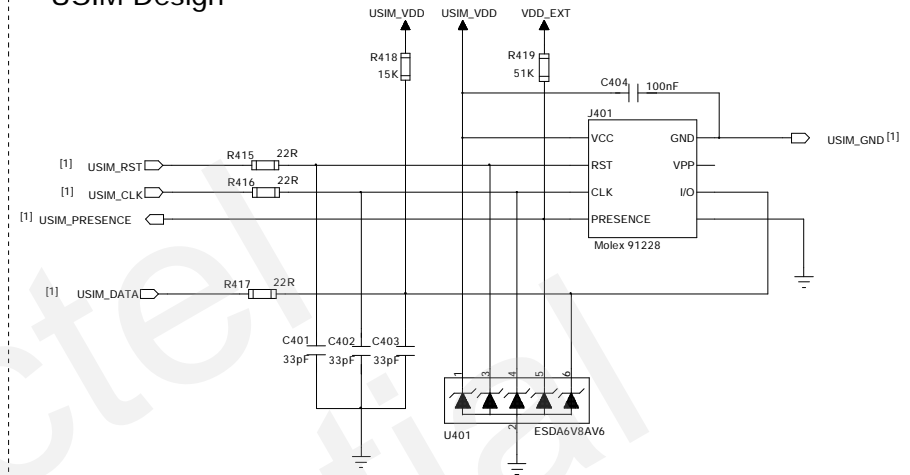
1. CTS&RTS is used for hardware flow control when mass data will be transferred.
2. RI will output an indication signal when there are activities such as voice calling, data calling and SMS.
3. DCD is mainly applied in the modem communication (PPP).
4. Please pay attention to the level match of UART in product application.

## Connection of Three Lines UART Interfaces for 3.3V System



\* Please pay attention to the level match of UART in product application.

## USIM Design



### Notes for USIM Card Layout

1. Place USIM card holder to the module as close as possible. Ensure the trace length of USIM signals do not exceed 200mm.
2. Keep the USIM signals far away from VBAT power and RF trace.
3. The width of USIM\_VDD and USIM\_GND trace is not less than 0.5mm. Place a bypass capacitor less than 1uF close to USIM card power pin.
4. To avoid cross talk between USIM\_DATA and USIM\_CLK, keep them away from each other and shield them with ground. Furthermore, the USIM\_RST should also be protected with ground.
5. In order to ensure good ESD performance, it is recommended to place a TVS array such as ESDA6V8AV6 as close to the card holder as possible. The capacitance of ESD component should be less than 50pF.
6. The 22Ω resistors should be added in series between the module and the SIM card so as to suppress the EMI for enhancing ESD protection. The 33pF capacitors are used for filtering interference of GSM900.

## Quectel Wireless Solutions

DRAWN BY <Huik Li>	PROJECT <UC15 Reference Design>	TITLE <UART and USIM Design>
CHECKED BY <Layne YE>	SIZE A2	VER 1.0
SHEET	4 of 5	<2013.11>

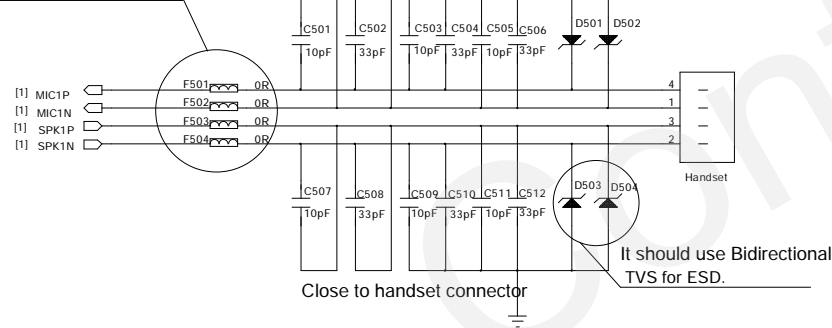
# Analog Audio Design

## Notes:

1. AGND is recommended to be routed separately.
2. 10P&33P capacitors are used for filtering RF noise.
3. Both AIN1 and AIN2 have microphone bias voltage inside module.
4. Both AOUT1 and AOUT2 are capable of driving 32ohm load, but not 8ohm.

## Handset Application of AIN1/AOUT1

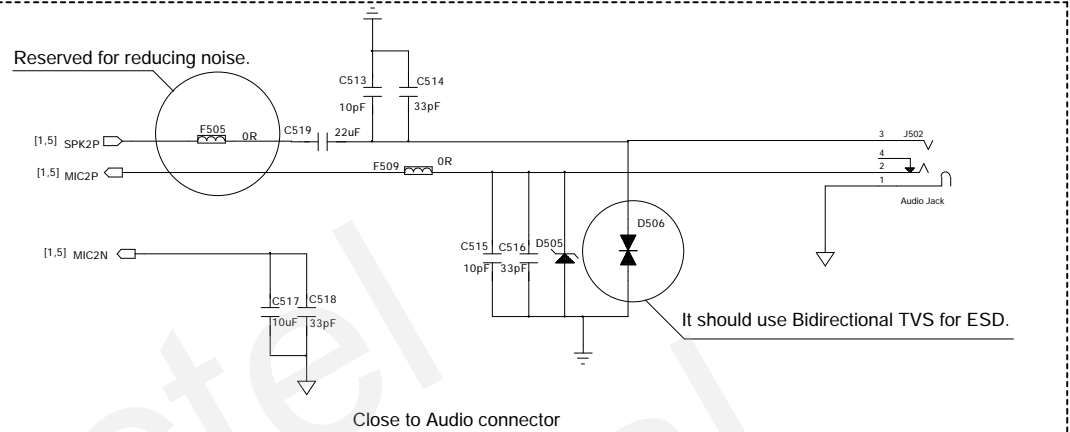
Reserved for reducing noise.



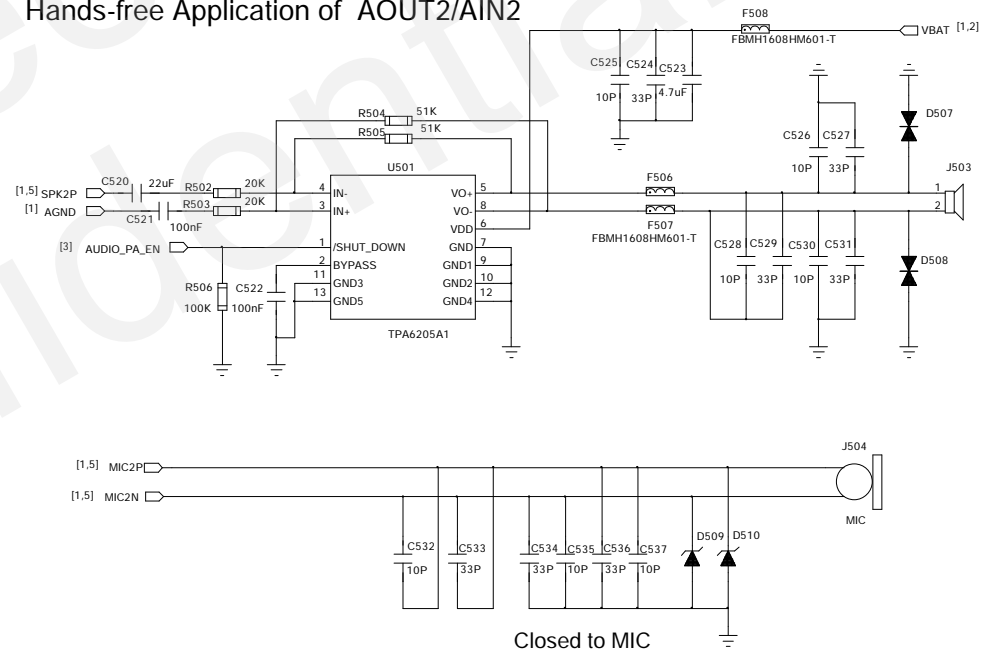
Close to handset connector

It should use Bidirectional TVS for ESD.

## Earphone Application of AIN2/AOUT2



## Hands-free Application of AOUT2/AIN2



## Quectel Wireless Solutions

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CHECKED BY <Layne YE>	SIZE A2	VER 1.0
SHEET 5 of 5		<2013.11>